



**Subject: TeamPoS 3600 – Legacy I/O Board Jumper Settings – Update A**

**Overview**

An error was found on the SPG and the Installation and Maintenance Manual in reference to the jumper settings on the Legacy I/O Board used on the TeamPoS 3600 terminal. Some earlier versions of this board also had the silkscreen printed on the board incorrectly. This caused some confusion as to which jumper was used for which purpose.

The jumpers that control the outputs on pin 1 and pin 9 of the Legacy I/O board were inadvertently reversed on the silkscreen on the earlier boards, including preproduction boards. This caused the documentation in the SPG and the Installation and Maintenance Guide to show the incorrect jumper positions on the current production boards.

The correct configuration for all current boards can be found on the following page.

**Impact**

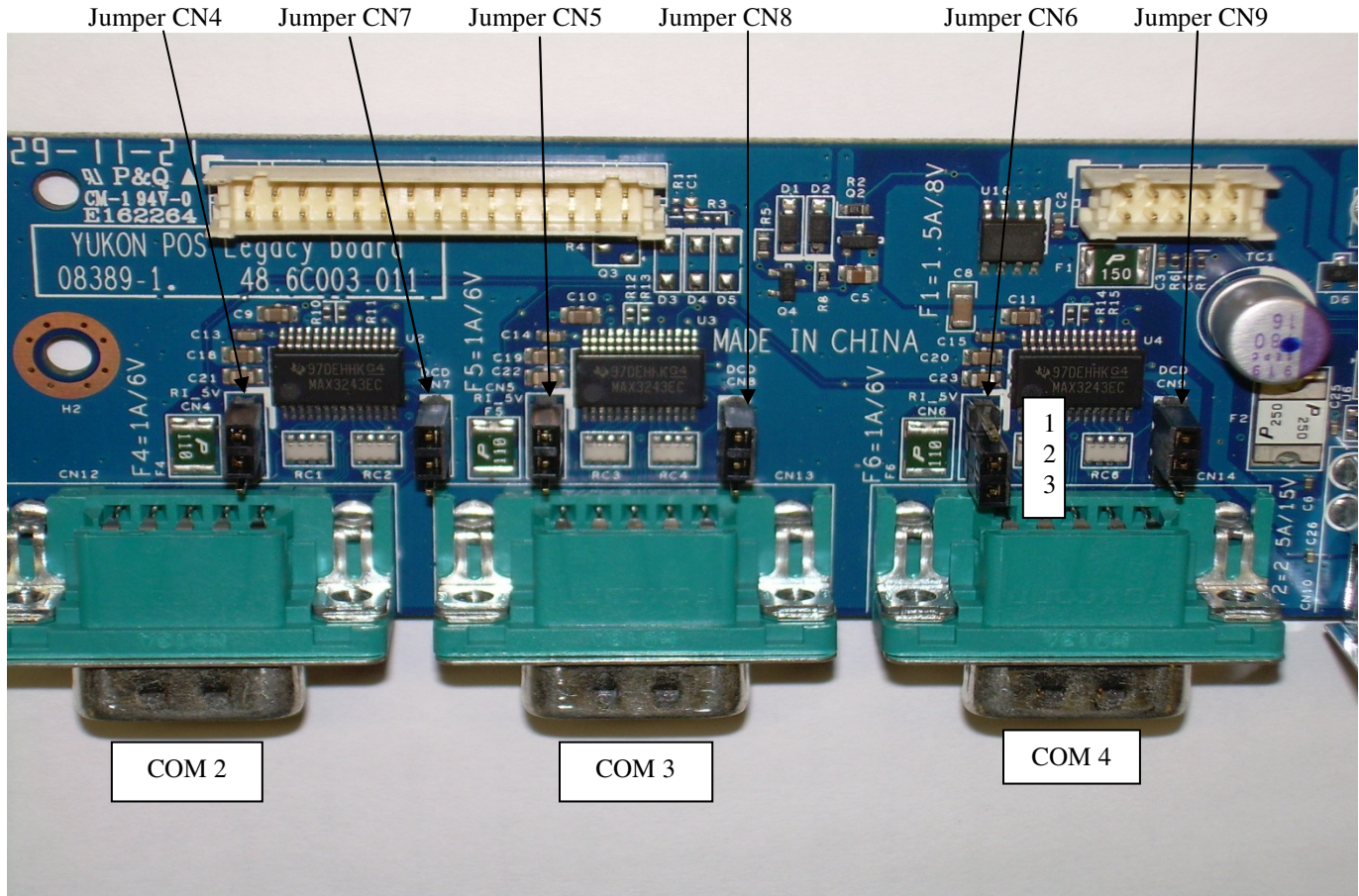
- Production
  - N/A
- Installed Units
  - Service persons and Installers need to be made aware of these differences and documentation errors.
- Repair Center
  - Upon receiving a preproduction board or early release board with the jumpers that do not match the picture on the following page, the board is to be scrapped.
- Service Spares
  - N/A

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**Legacy I/O Board Jumpers**

The Legacy I/O board contains six jumpers. Three of the jumpers (CN4, CN5, & CN6) will either provide +5 V on pin 9 of DB9 connector or provide the RI signal on that pin depending upon the jumper setting. If pins 1 & 2 are jumpered on CN4, CN5, or CN6 it provides (RI) on pin 9 of COM2, 3, or 4 respectively. If pins 2-3 are jumpered, it provides +5V to pin9 of the DB9 connector. Default is set as RI (pins 1&2 shorted) as shown in picture for CN4 and CN5. CN6 controls COM4 and is set for +5V in the picture below. Jumpers CN7, CN8, and CN9 enable the DCD signal on pin 1 of the DB9 connector and should remain in this default position if DCD is required.

The jumper designations below are current production. The early boards, the designation was different but the jumper function per physical location was the same.



**Legacy I/O Board**

Pin	COM 2/3/4
1	Data Carrier Detect (DCD)
2	RX
3	TX
4	DTR
5	Ground
6	DSR
7	RTS
8	CTS
9	RI or +5V – Jumper Select

Pin 9 Function Selection				
Jumper	COM	1-2	2-3	Default
CN4	2	RI	+5V	1-2
CN5	3	RI	+5V	1-2
CN6	4	RI	+5V	1-2
Pin 1 Function Selection				
Jumper	COM	1-2	2-3	Default
CN7	2	DCD	Open	1-2
CN8	3	DCD	Open	1-2
CN9	4	DCD	Open	1-2